

# A chip-scale imprinter with integrated optical interference for calibrating models of NIL resists and resist-stamp boundary conditions

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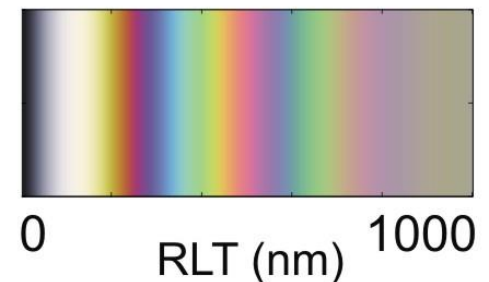
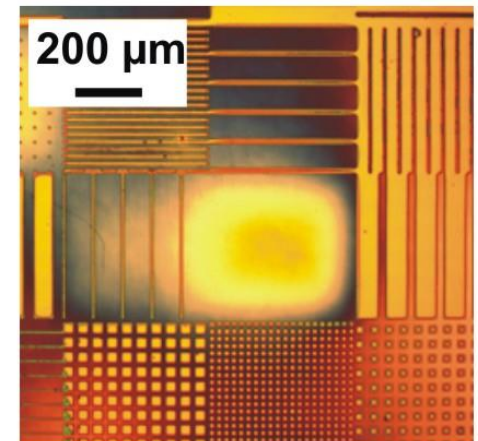
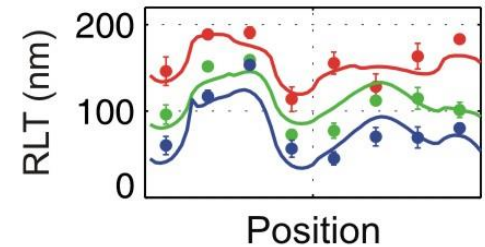
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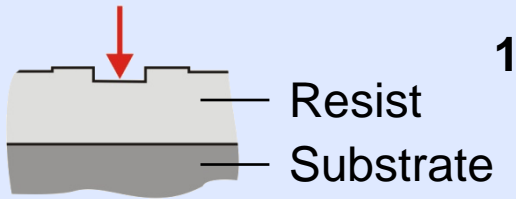
# Outline

- Resist model calibration by fitting simulations to imprint experiments
- The need for new tools to characterise resist–stamp material combinations
- Real-time optical monitoring of imprint
- Potential RLT sensing enhancements:
  - Plasmonic
  - Ellipsometric
- Potential applications of real-time optical imprint monitoring:
  - Endpoint detection
  - Process control
  - Defect detection (including non-fill)

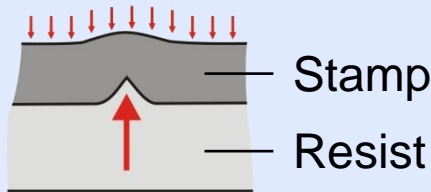


# Our existing simulation technique quickly finds RLT and cavity-filling distributions

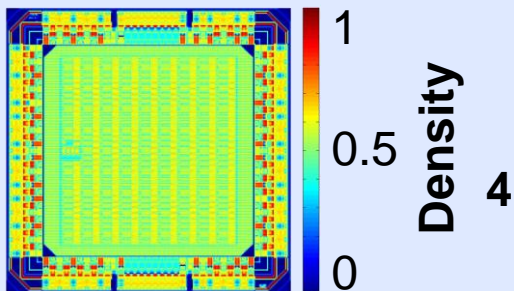
## Resist surface's impulse response



## Stamp's load response (bending, indentation)<sup>2</sup>



## Pattern abstraction

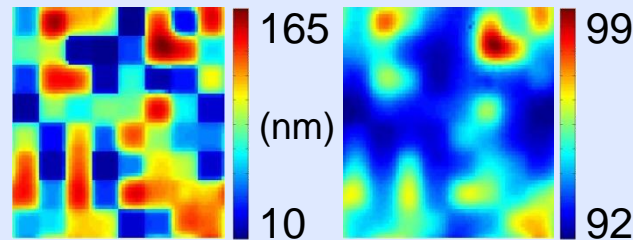


## Example questions:

*Does changing stamp material affect residual layer uniformity?*<sup>1,2</sup>

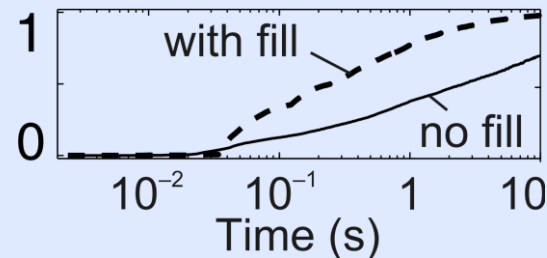
Elastomer

Silicon

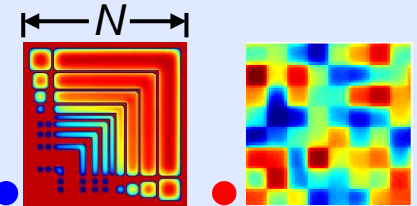
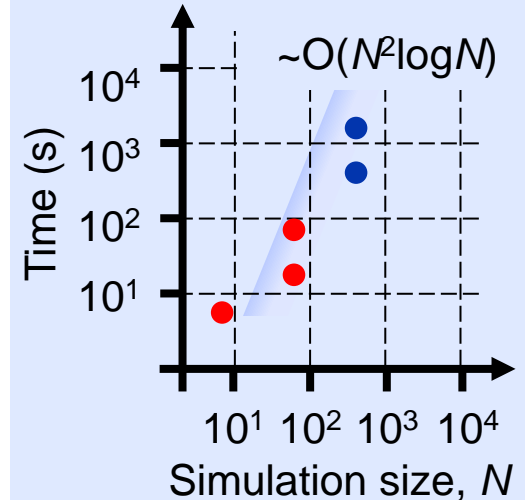


*Can 'dummy fill' accelerate stamp cavity filling?*<sup>3</sup>

Proportion cavities filled



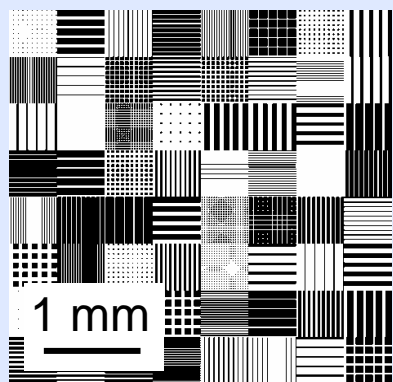
## Simulations need to be highly scalable



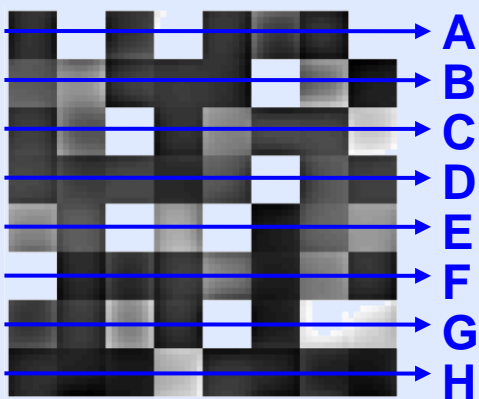
- At least 10<sup>3</sup> times faster than FEM
- Can trade off spatial resolution and speed

# The NIL simulation technique has been experimentally validated

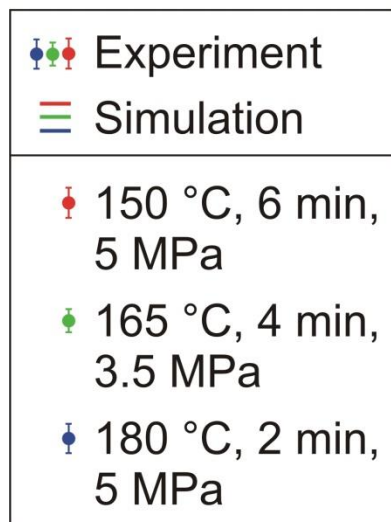
Silicon test stamp:



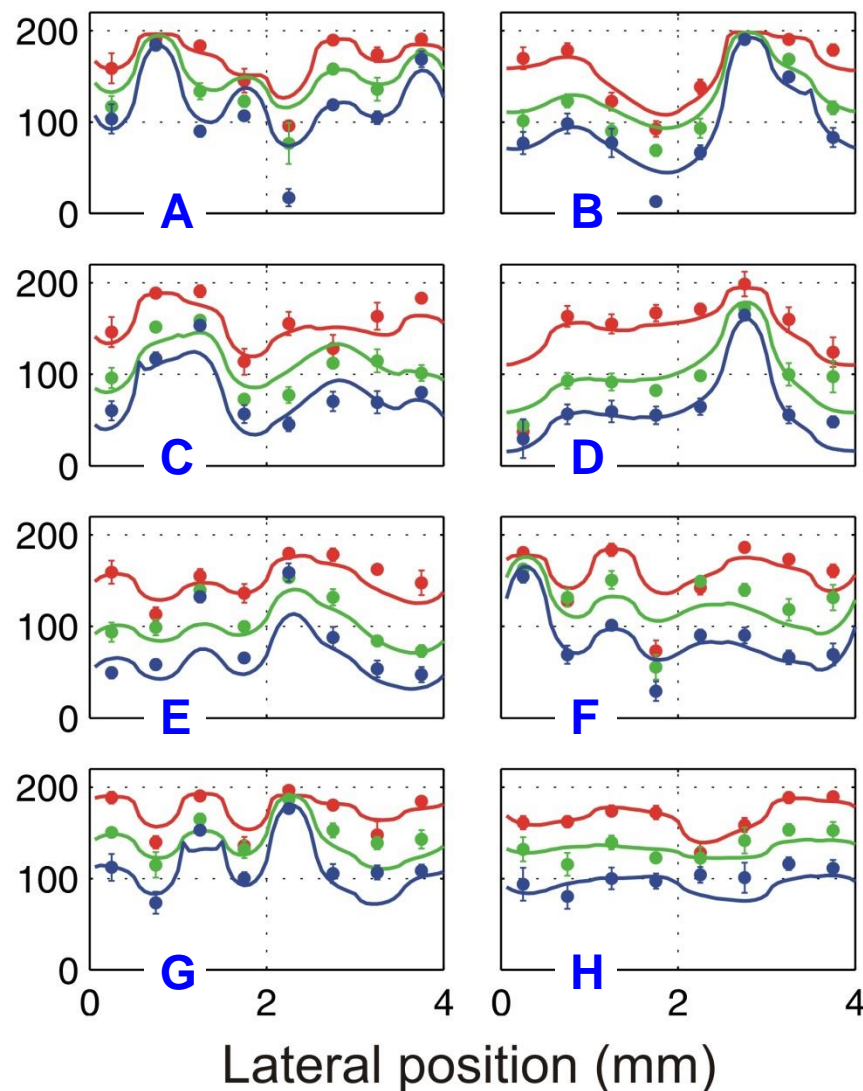
■ Cavities  
(~500 nm deep)  
□ Protrusions



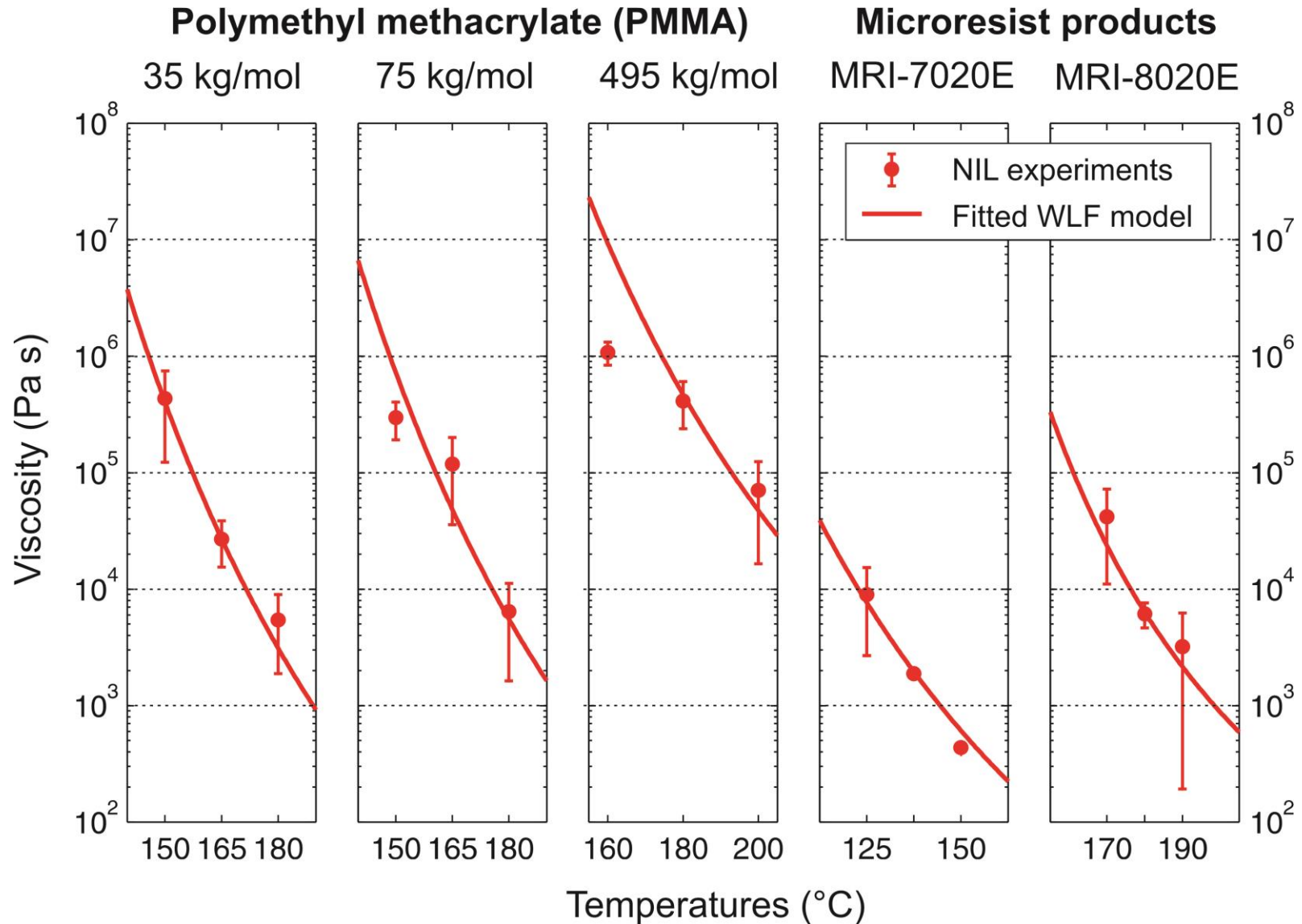
Residual  
layer  
thicknesses  
(nm)



PMMA 35 kg/mol

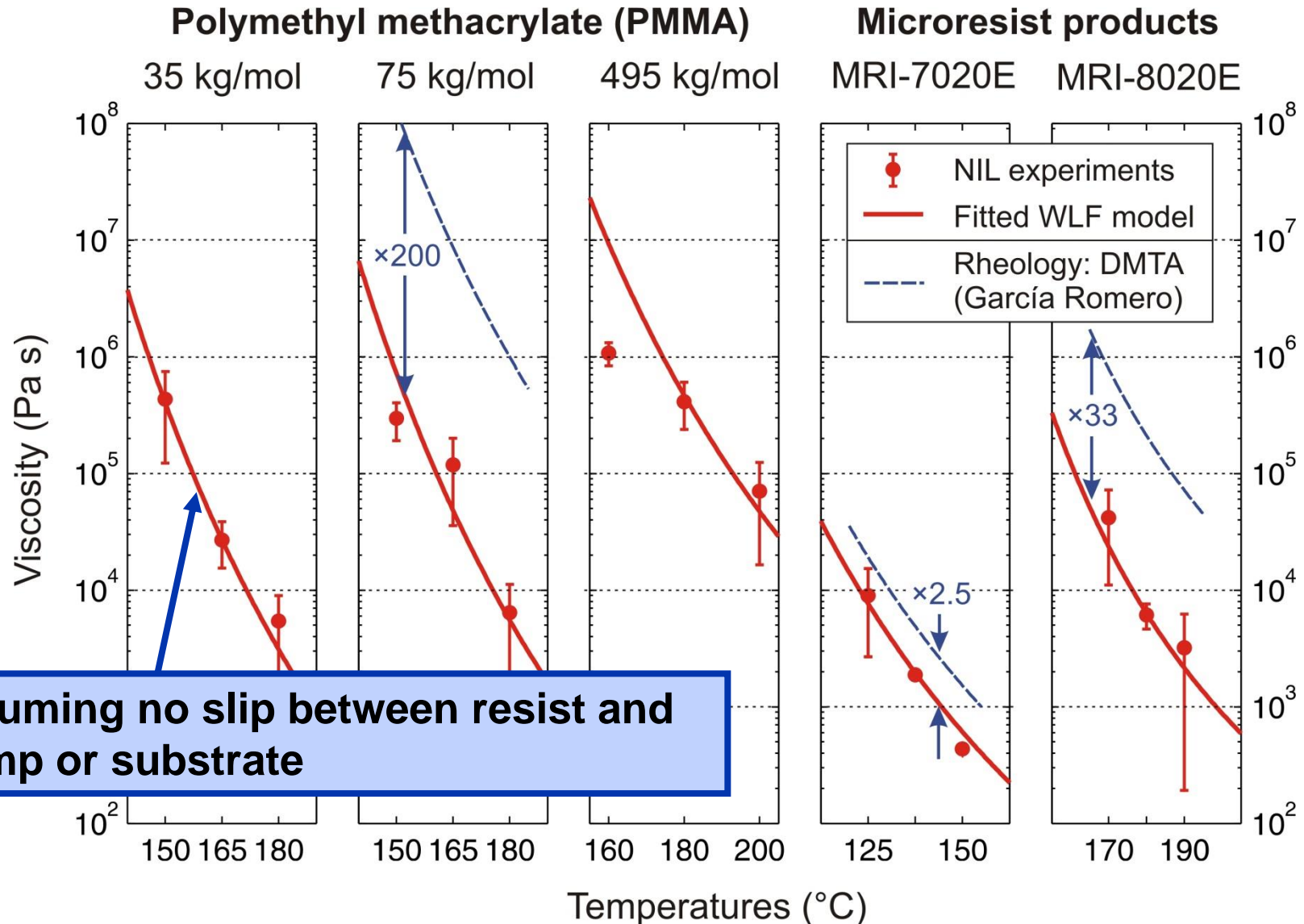


# The technique has been validated for five thermoplastic materials



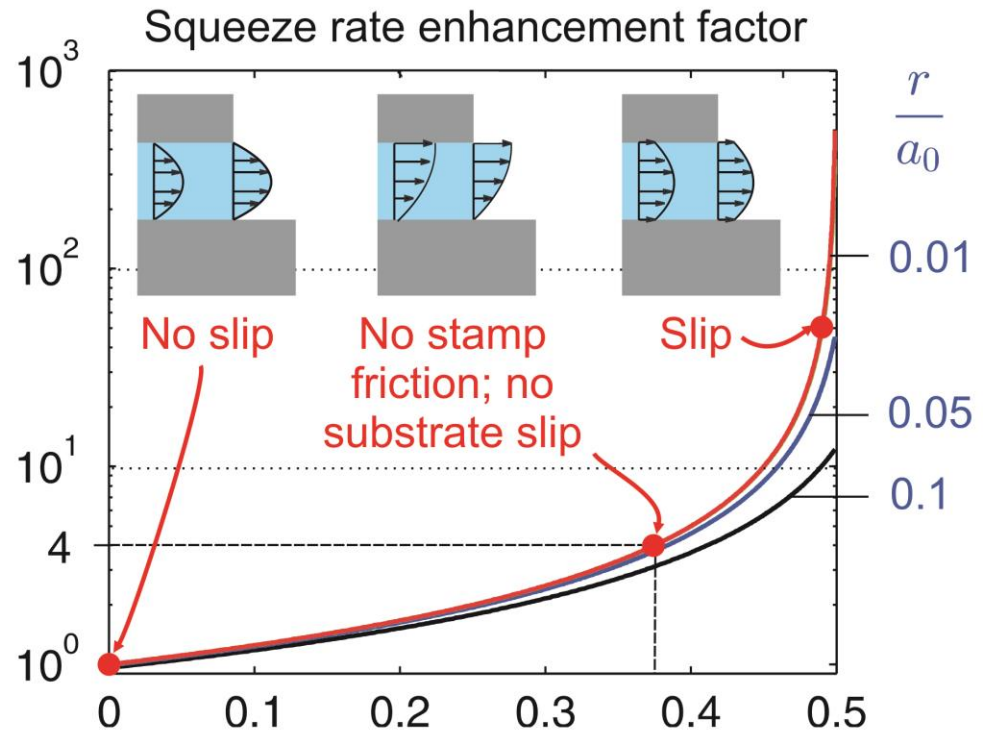
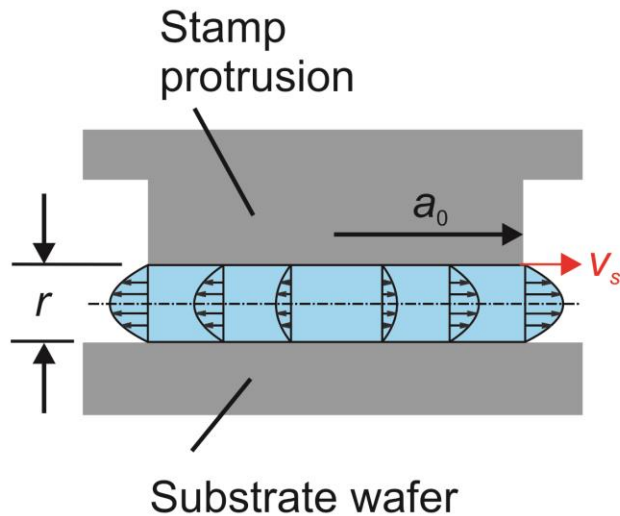


# Nanoscale experiments may involve substantial slip between resist and stamp



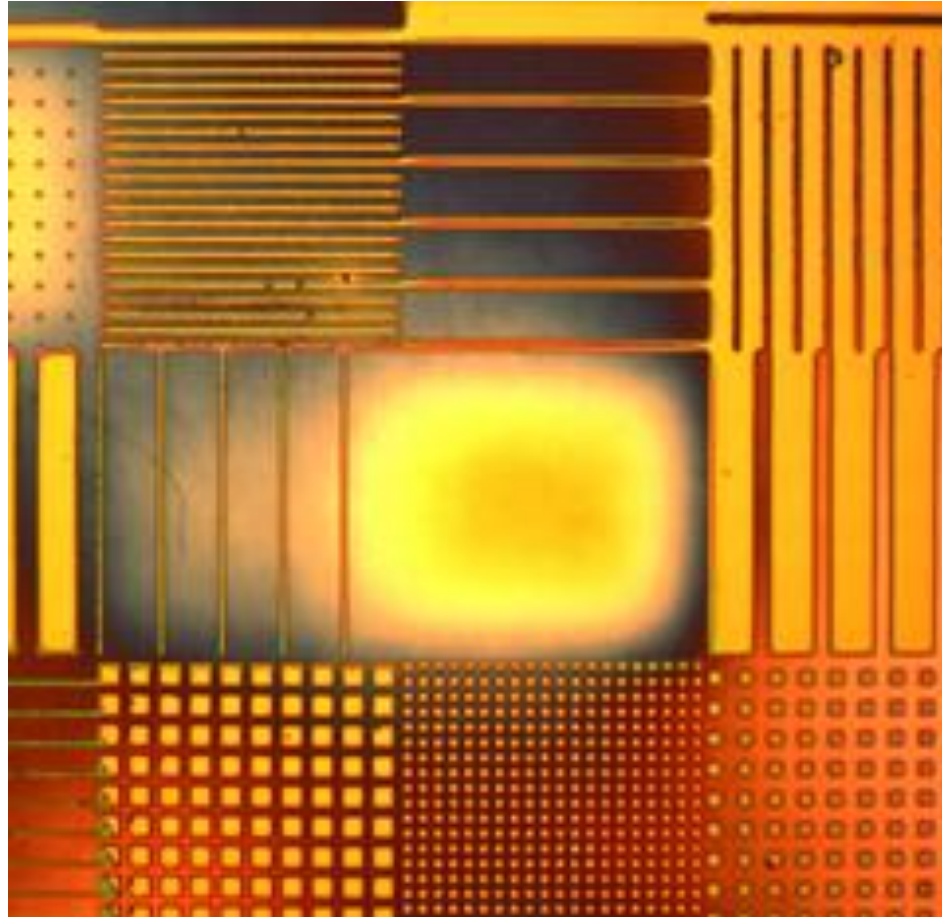
# The imprinting rate will depend strongly on any slip between resist and stamp/substrate

$$\frac{dr}{dt} = -\frac{r^3 p_0}{4k\eta_0 a_0^2} \left[ (1 - 2\delta) + \left(\frac{r}{a_0}\right)^2 (1 + 2\delta) \right]^{-1}$$



Degree of slip,  $\delta \equiv -\frac{r}{a_0} \frac{v_s}{\dot{r}}$

# *Real-time* observation of evolving RLT could accelerate material/stamp characterisation

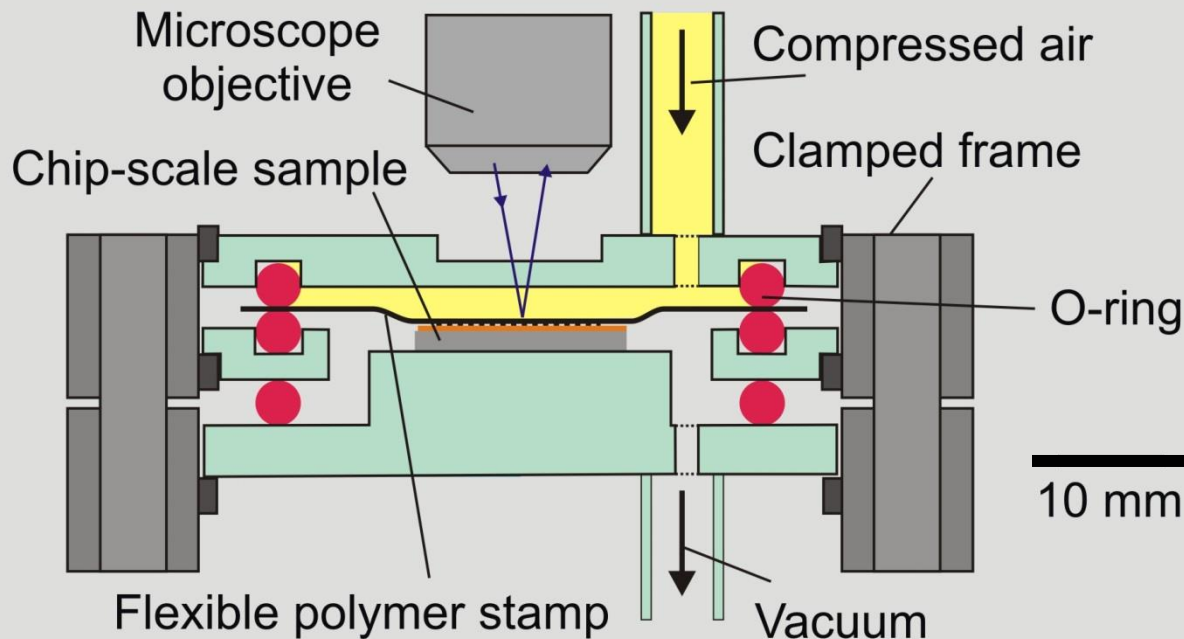
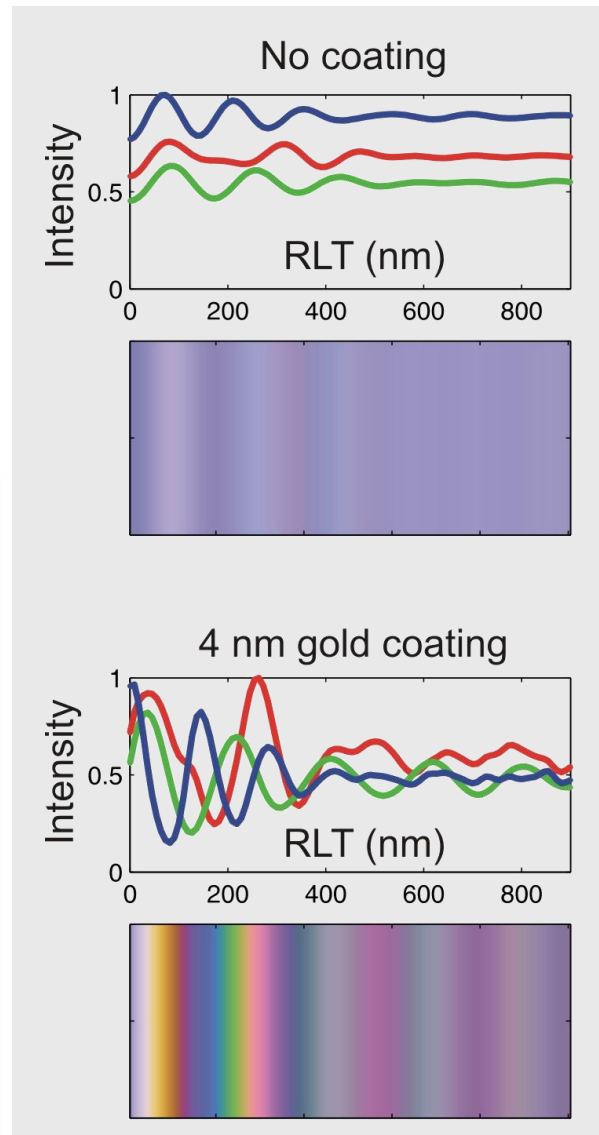
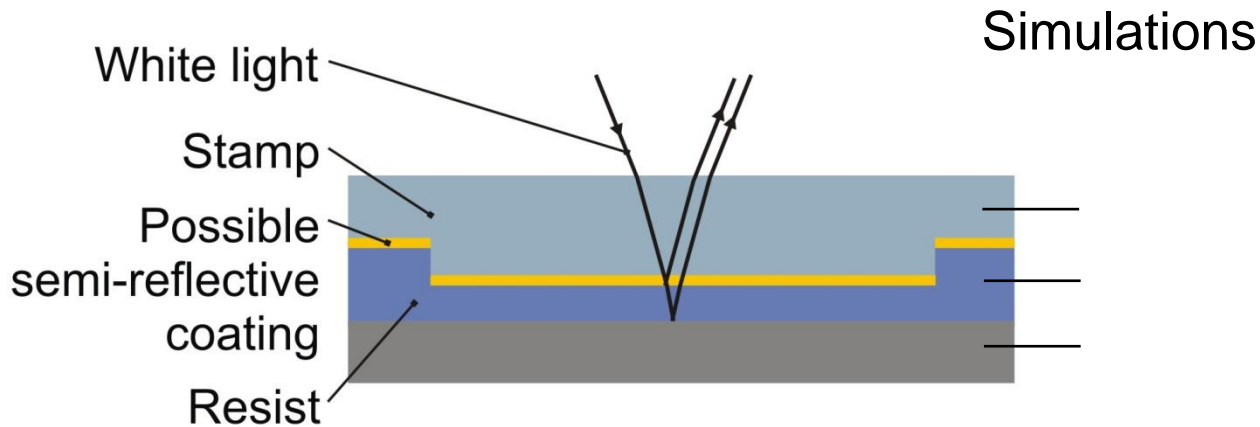


100  $\mu\text{m}$

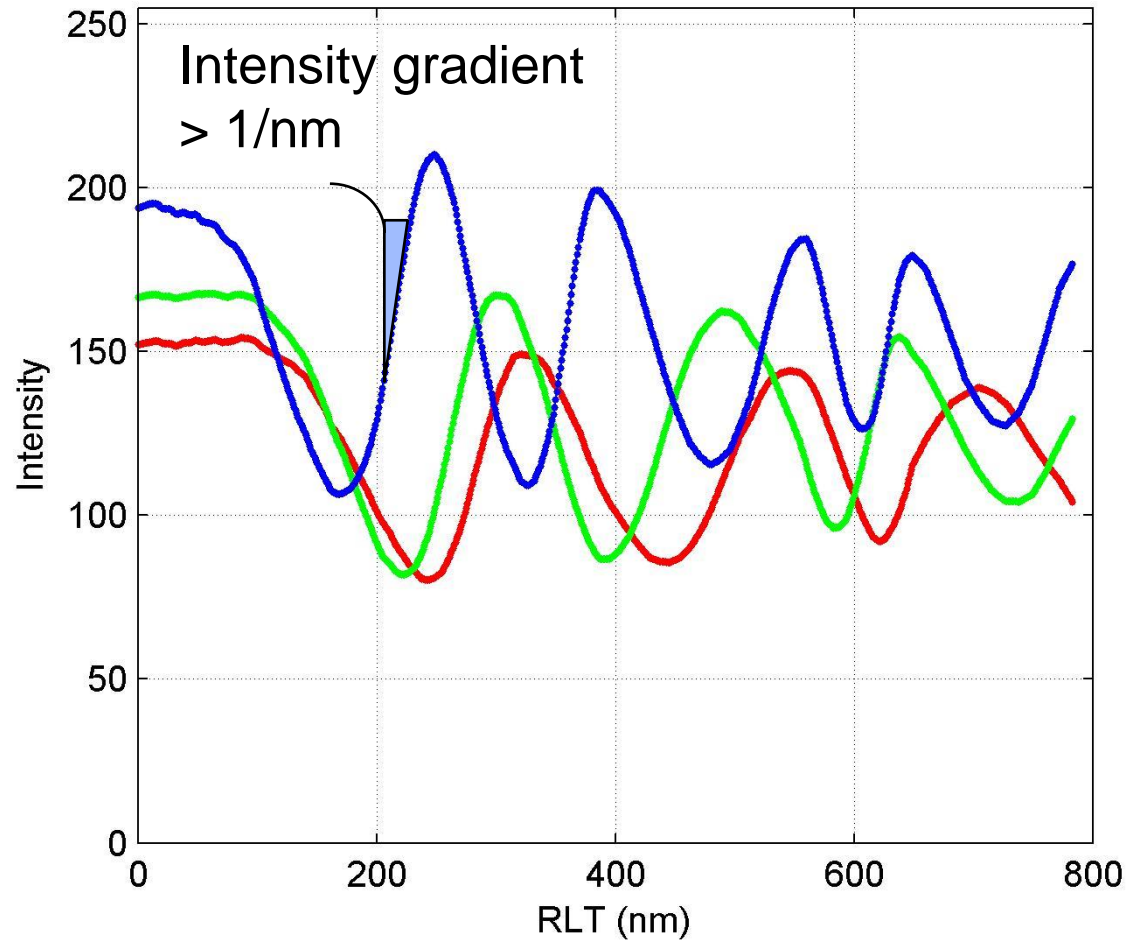
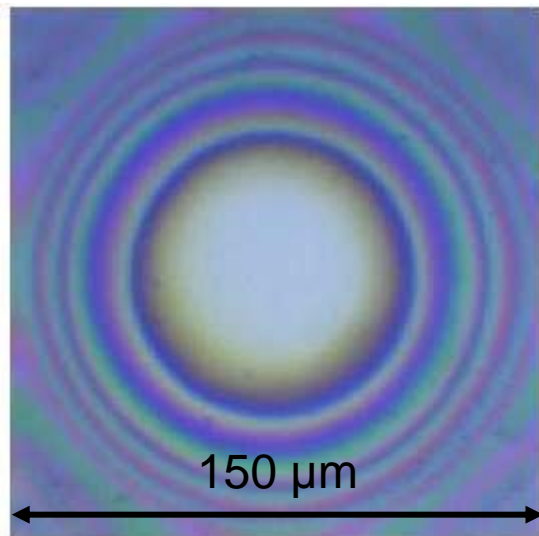
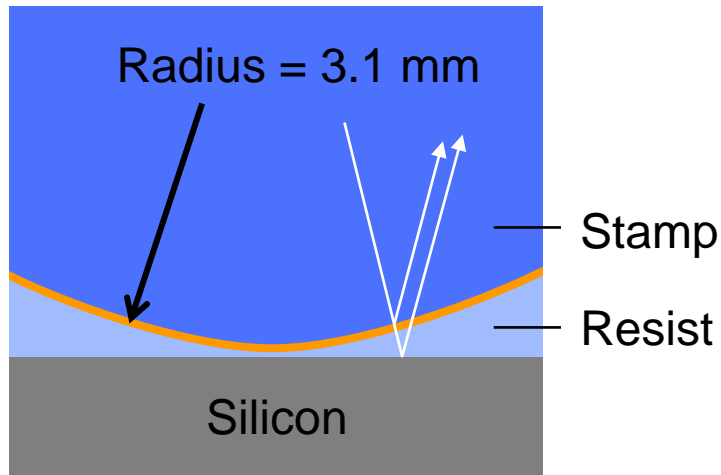




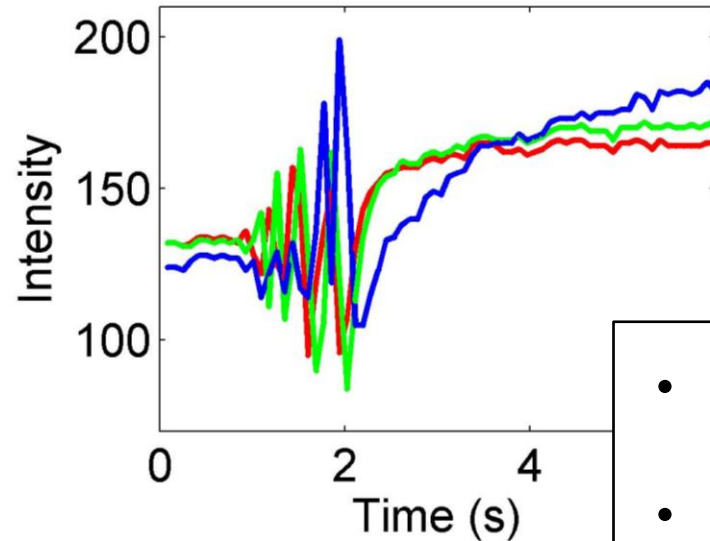
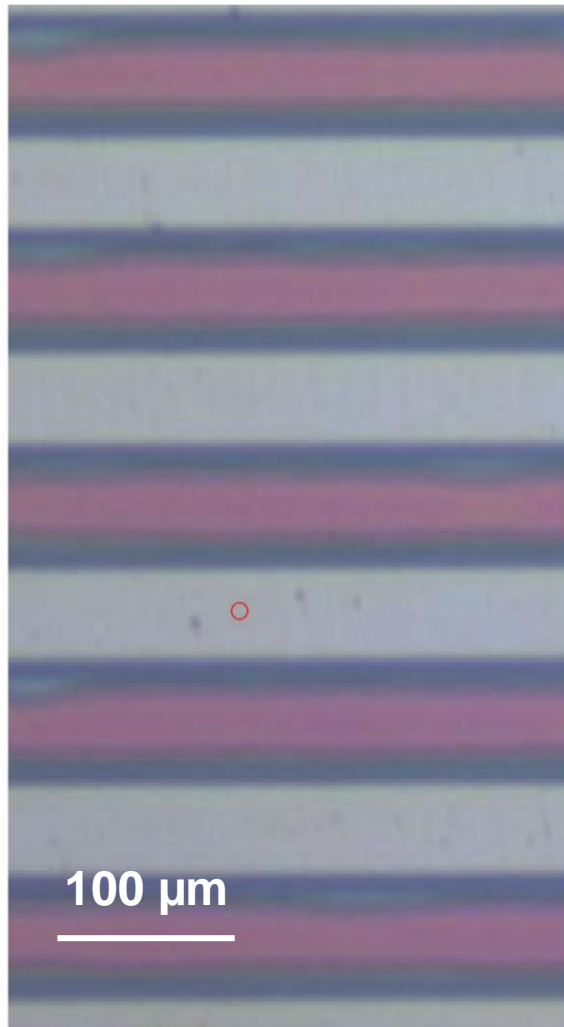
# Real-time observation of evolving RLT could accelerate material/stamp characterisation



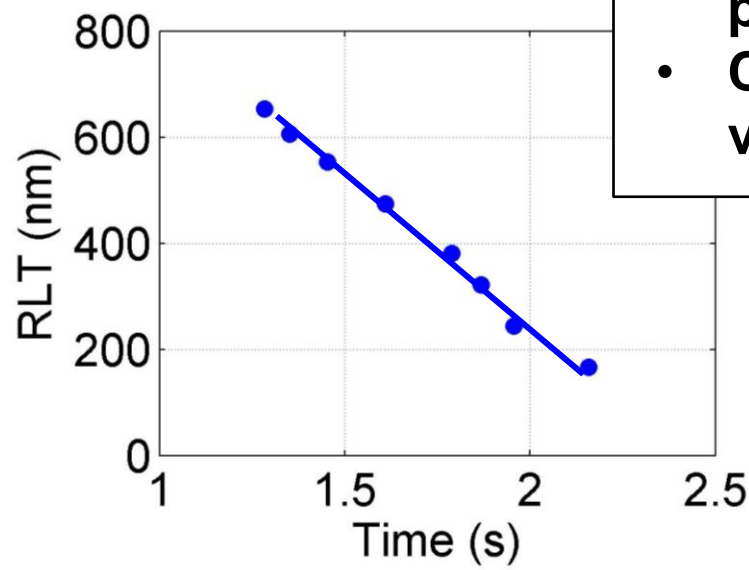
# RLT-colour relationships are calibrated using a known stamp topography



# Videos of the imprinting process allow the temporal response of resist to be extracted

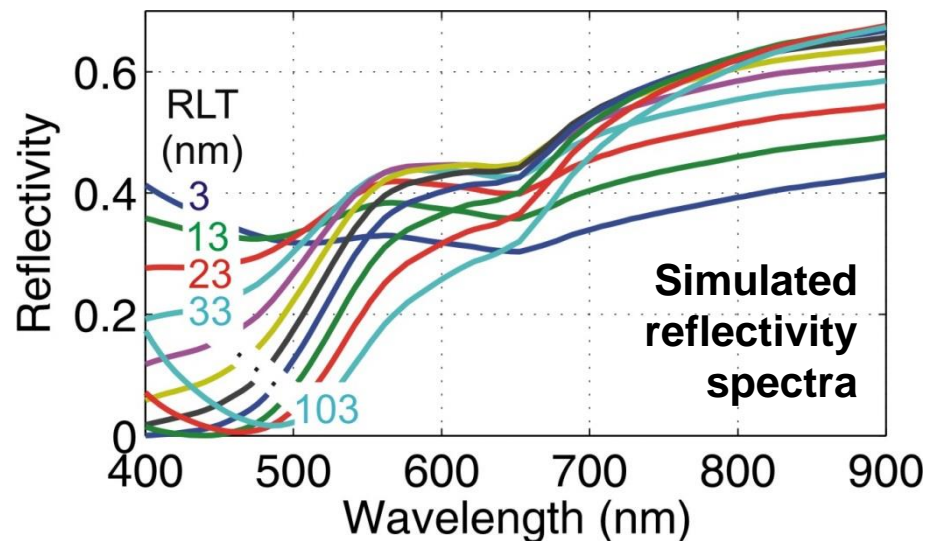
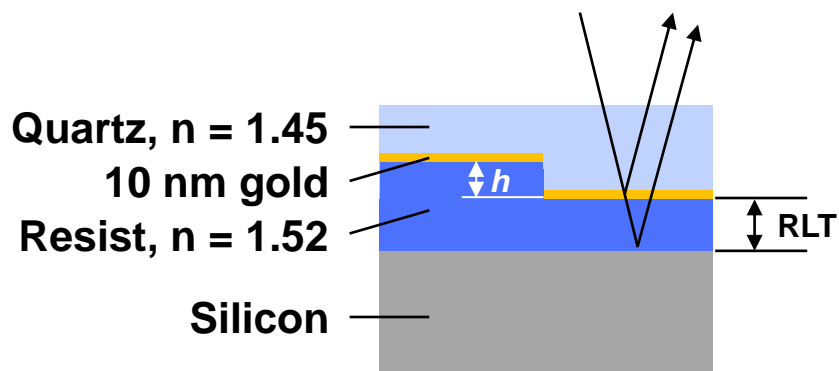


- **Material:**  
**MRT mr-UVCur-21**
- **Stamp-average pressure 20 kPa**
- **Consistent with viscosity ~ 7 Pa.s**

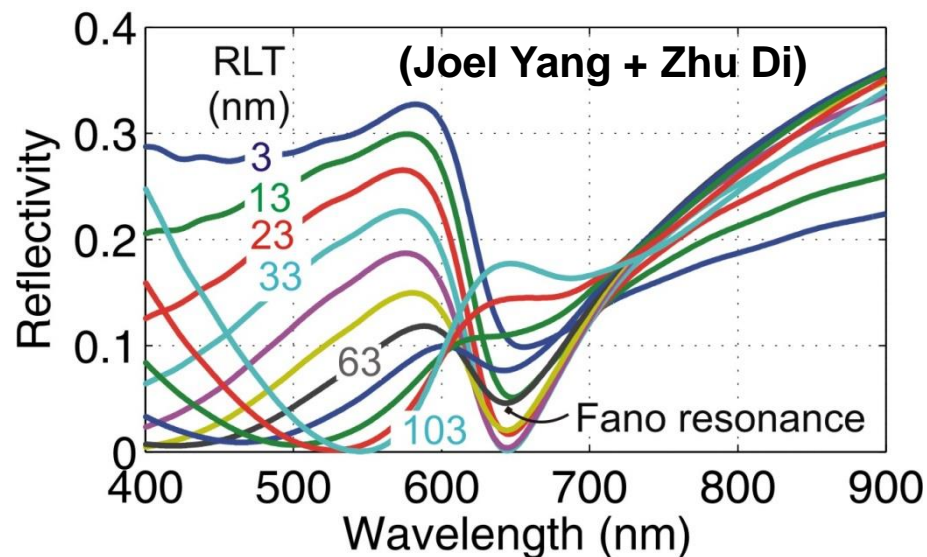
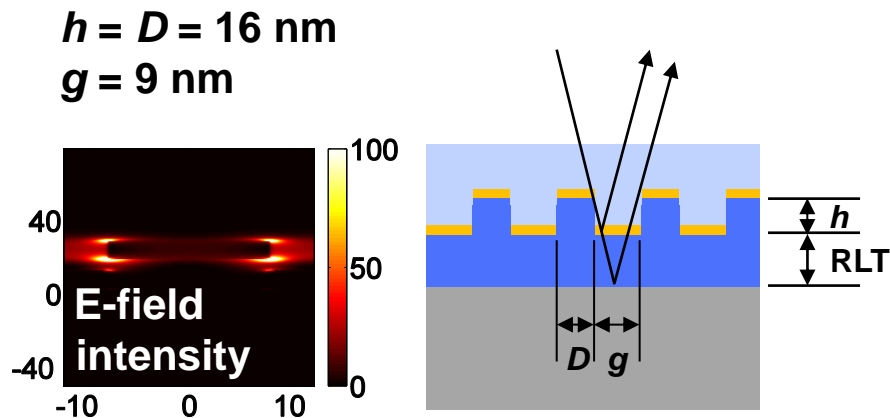


# If features are sub-wavelength, plasmonic effects could enhance RLT detection

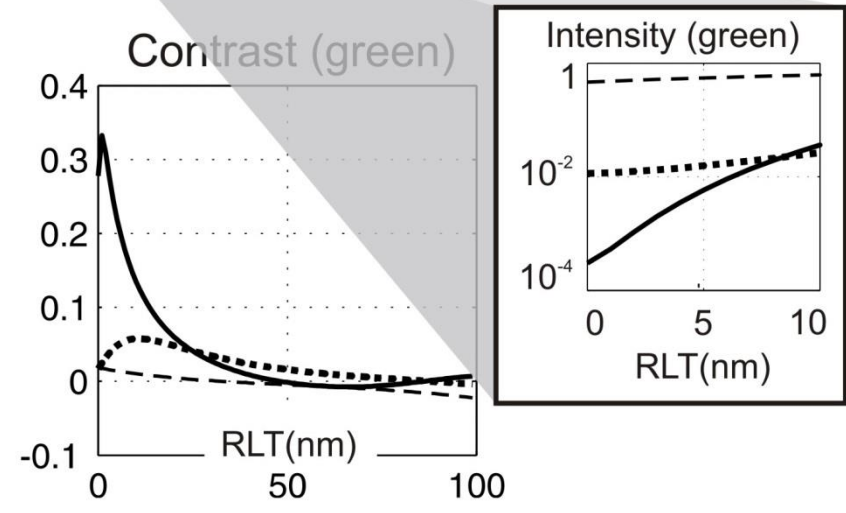
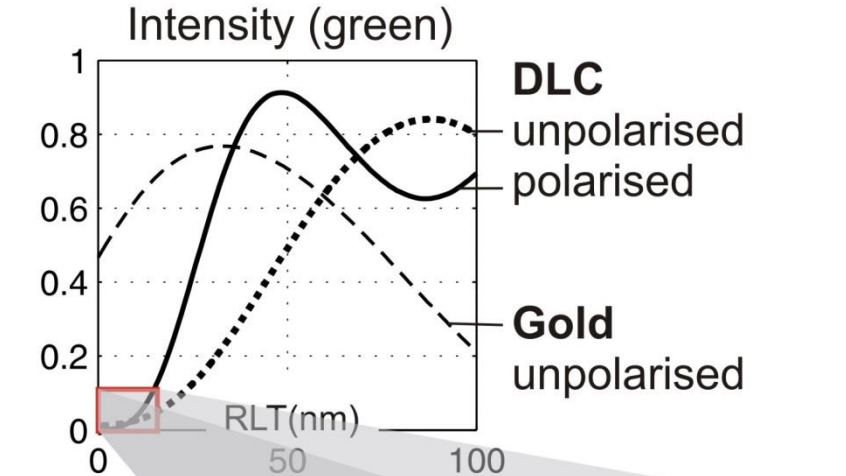
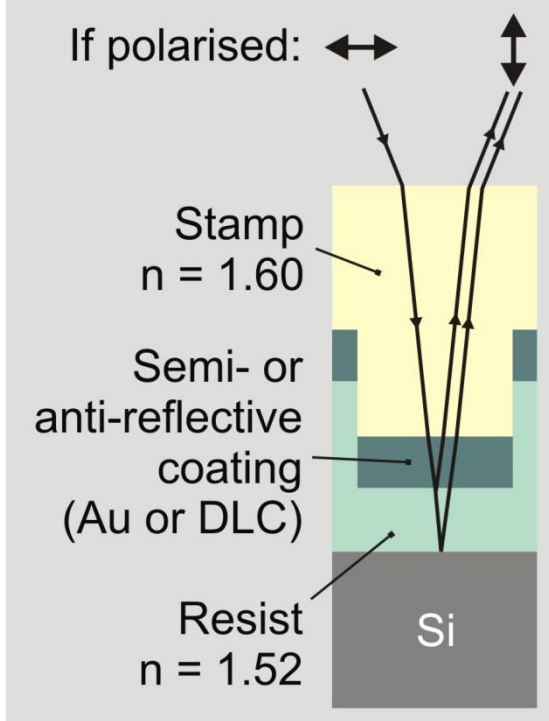
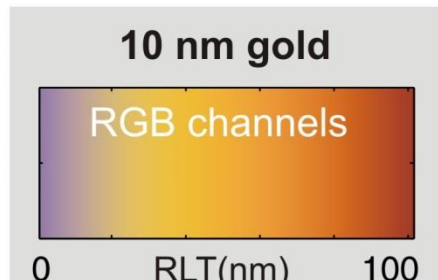
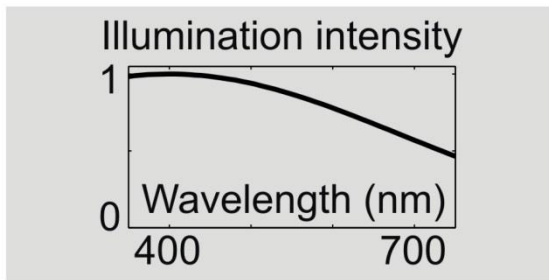
## Large features



## 25 nm-pitch dot array



# An anti-reflective stamp coating and light polarisation could enhance RLT contrast



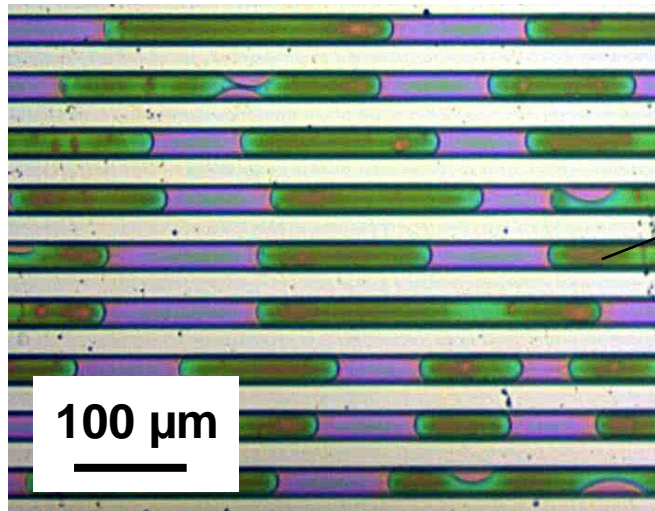
**Contrast:** relative change in intensity for a 1 nm change in RLT about a given RLT

**Substrate-enhanced ellipsometric contrast:** Ausserré, *Optics Express*, **15** 8329 (2007)



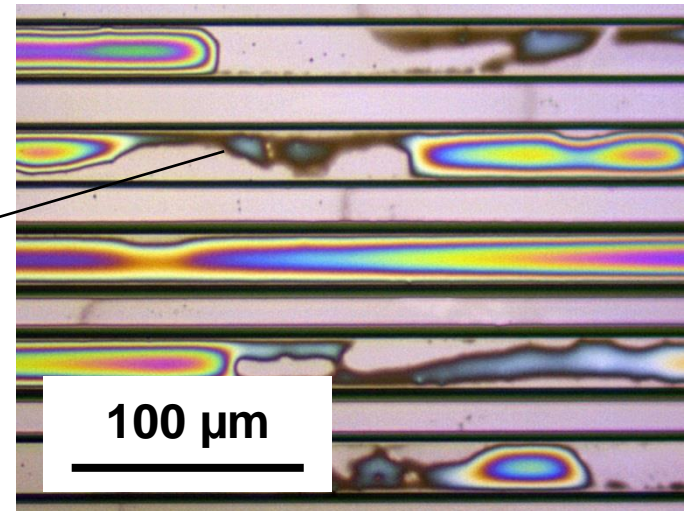
# Potential applications include endpoint detection and cavity filling monitoring

- Endpoint detection
- Feedback control in R2R processing
- Cavity filling
- Stamp inspection/defect detection
- Probing of complex fluids (e.g. biological samples)



Partially  
filled  
cavity

Resist  
residue



# Conclusions and outlook

- Resist rheological behaviour can be extracted from nanoscale imprint experiments
- Resist-stamp boundary conditions are critical and require characterising
- Real-time measurement of RLT can be accomplished interferometrically using simple white light microscopy
- Optical RLT measurement sensitivity can be tuned using plasmonic or ellipsometric approaches
- Real-time RLT measurement could be applied to improve NIL yield and throughput

# Collaborators and acknowledgements

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